

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3524	mtj or (magnetic adj tunnel adj junction)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:54
L2	576762	diode	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:54
L3	860	1 and 2	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:54
L4	68490	leakage adj current	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:54
L5	141	3 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:54
L6	31451	write near10 current	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:55
L7	68	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:55
L8	307873	margin	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:55
L9	28	7 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:55
L10	41	eaton-james-r\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:55
L11	339	perner-frederick-a\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:55

EAST Search History

L12	188	tran-lung-t\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:56
L13	83	eldredge-kenneth-j\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/06/07 08:56

Day : Thursday

Date: 6/7/2007

Time: 17:05:44

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = EATON

First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11215531	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11215627	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11215758	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
08859830	5930065	150	05/16/1997	TIMING BASED SERVO LONGITUDINAL ADDRESSING	EATON, JAMES HOWARD
08865540	6021013	150	05/29/1997	TIMING BASED SERVO SYSTEM FOR MAGNETIC TAPE SYSTEMS	EATON, JAMES HOWARD
08896356	5883770	250	07/18/1997	PARTIAL WIDTH MASS PRODUCED LINEAR TAPE RECORDING HEAD	EATON, JAMES HOWARD
08896782	5905613	150	07/18/1997	BIDIRECTIONAL FLAT CONTOUR LINEAR TAPE RECORDING HEAD AND DRIVE	EATON, JAMES HOWARD
09067490	6043963	150	04/27/1998	LOW-PROFILE TAPE CARTRIDGE	EATON, JAMES HOWARD
09188997	6341416	150	11/09/1998	METHOD FOR MASS PRODUCING PARTIAL WIDTH LINEAR TAPE RECORDING HEAD	EATON, JAMES HOWARD
09420889	6282051	150	10/19/1999	TIMING BASED SERVO SYSTEM FOR MAGNETIC	EATON, JAMES HOWARD

				TAPE SYSTEMS	
<u>11498597</u>	Not Issued	30	08/02/2006	Information system and method for generating appraisal reports for real properties	EATON, JAMES M.
<u>08198714</u>	Not Issued	166	02/18/1994	POWER CONTROL SYSTEM FOR A THERMAL INK-JET PRINTER	EATON, JAMES N.
<u>08509396</u>	<u>5563635</u>	150	07/31/1995	POWER CONTROL SYSTEM FOR A THERMAL INK-JET PRINTER	EATON, JAMES N.
<u>07741000</u>	Not Issued	161	08/06/1991	RADIO-CONTROLLED MODEL SUSPENSION SYSTEM	EATON, JAMES O.
<u>07997858</u>	<u>5312044</u>	150	12/29/1992	PRESSURE CLEANING METHOD AND APPARATUS	EATON, JAMES O.
<u>08368488</u>	<u>5833514</u>	250	01/04/1995	REACTIONARY FORCE UTILIZATION	EATON, JAMES O.
<u>09934548</u>	<u>6864529</u>	150	08/23/2001	THIN FILM TRANSISTOR MEMORY DEVICE	EATON, JAMES R.
<u>09976304</u>	<u>6501697</u>	150	10/11/2001	HIGH DENSITY MEMORY SENSE AMPLIFIER	EATON, JAMES R.
<u>10006723</u>	<u>6661704</u>	150	12/10/2001	DIODE DECOUPLED SENSING METHOD AND APPARATUS	EATON, JAMES R.
<u>10138076</u>	<u>6760245</u>	150	05/01/2002	MOLECULAR WIRE CROSSBAR FLASH MEMORY	EATON, JAMES R.
<u>10145337</u>	<u>6738307</u>	150	05/13/2002	ADDRESS STRUCTURE AND METHODS FOR MULTIPLE ARRAYS OF DATA STORAGE MEMORY	EATON, JAMES R.
<u>10151913</u>	Not Issued	161	05/22/2002	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	EATON, JAMES R.
<u>10346494</u>	<u>6839270</u>	150	01/17/2003	SYSTEM FOR AND METHOD OF ACCESSING A FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY	EATON, JAMES R.
<u>10346700</u>	<u>6842389</u>	150	01/17/2003	SYSTEM FOR AND METHOD OF FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY CELL AND DECODING SCHEME	EATON, JAMES R.
<u>10661448</u>	Not Issued	30	09/11/2003	Increased magnetic memory array sizes and operating margins	EATON, JAMES R.
<u>10676465</u>	<u>6927996</u>	150	09/30/2003	MAGNETIC MEMORY	EATON, JAMES R.

				DEVICE	
<u>10697776</u>	Not Issued	161	10/30/2003	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	EATON, JAMES R.
<u>10777000</u>	<u>6781918</u>	150	02/10/2004	ADDRESS STRUCTURE AND METHODS FOR MULTIPLE ARRAYS OF DATA STORAGE MEMORY	EATON, JAMES R.
<u>10985762</u>	<u>7161838</u>	150	11/09/2004	THIN FILM TRANSISTOR MEMORY DEVICE	EATON, JAMES R.
<u>08199400</u>	<u>5445043</u>	150	02/22/1994	HYDRAULIC SHIFT CONTROL FOR A POWER TRANSMISSION	EATON, JAMES R.
<u>08304892</u>	<u>5509868</u>	150	09/13/1994	GOVERNOR INTERLOCK VALVE	EATON, JAMES R.
<u>08310448</u>	<u>5496231</u>	150	09/22/1994	HYDRAULIC CONTROL FOR A MULTI-SPEED POWER TRANSMISSION	EATON, JAMES R.
<u>08310456</u>	<u>5492027</u>	150	09/22/1994	ROTARY SHIFT CONTROL VALVING MECHANISM FOR A POWER TRANSMISSION	EATON, JAMES R.
<u>10347723</u>	<u>6826112</u>	150	01/21/2003	LOW POWER LOGIC GATE	EATON, JAMES ROBERT
<u>10348645</u>	<u>6940770</u>	150	01/21/2003	METHOD FOR PRECHARGING WORD AND BIT LINES FOR SELECTING MEMORY CELLS WITHIN A MEMORY ARRAY	EATON, JAMES ROBERT
<u>11129269</u>	Not Issued	95	05/12/2005	SELF-TIMED MEMORY DEVICE PROVIDING ADEQUATE CHARGING TIME FOR SELECTED HEAVIEST LOADING ROW	EATON, JAMES ROBERT
<u>08310447</u>	<u>5492210</u>	150	09/22/1994	RATIO INTERCHANGE CONTROL VALVING	EATON, JAMES RUSSELL
<u>06373861</u>	<u>4468903</u>	150	05/03/1982	BUILDING PANEL	EATON, JAMES W.
<u>06374190</u>	<u>4468909</u>	150	05/03/1982	BUILDING PANEL	EATON, JAMES W.
<u>06374281</u>	<u>4422266</u>	150	05/03/1982	BUILDING PANEL	EATON, JAMES W.
<u>06427543</u>	<u>4464872</u>	150	09/29/1982	BUILDING PANEL	EATON, JAMES W.

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="eaton"/>	<input type="text" value="james"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007
Time: 17:05:59 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = EATON

First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08198756	5519419	150	02/18/1994	CALIBRATION SYSTEM FOR A THERMAL INK-JET PRINTER	EATON, JAMES
08241439	Not Issued	161	05/11/1994	CHIP LEVEL CONTROL OF SPOT SIZE IN INK JET PRINTER	EATON, JAMES
08252144	5490020	150	06/01/1994	HIGH TORSIONAL STIFFNESS LOW AXIAL DEFLECTION STIFFNESS DRIVE SHAFT SYSTEM	EATON, JAMES
08368929	5739830	150	01/05/1995	MONOLITHIC PRINTHEADS FOR INK JET PRINTING APPARATUS	EATON, JAMES
09055016	6003802	150	04/03/1998	TAPE LEADER PIN ASSEMBLY AND METHOD FOR MAKING THE SAME	EATON, JAMES
09230964	6106067	150	08/16/1999	SEAT ADJUSTMENT AND DUMPING MECHANISM WITH MEMORY ADJUSTMENT COORDINATED WITH SEAT POSITIONING	EATON, JAMES
09320238	6499684	150	05/26/1999	TAPE LEADER PIN ASSEMBLY AND METHOD FOR MAKING THE SAME	EATON, JAMES
09706039	6513875	150	11/03/2000	FOLD-FLAT RECLINING MECHANISM FOR A VEHICLE SEAT	EATON, JAMES
60466544	Not Issued	159	04/29/2003	Crop thinning apparatus and method	EATON, JAMES
08577803	5653342	150	12/22/1995	ENCLOSURE FOR CARRYING AND PROTECTING A GOLF SCORECARD	EATON, JAMES B.
29211941	Not Issued	161	08/24/2004	Scope for firearms	EATON, JAMES BARTON

<u>10977983</u>	Not Issued	41	10/29/2004	Computerized method and system for coding-based navigation	EATON, JAMES D.
<u>10977985</u>	Not Issued	30	10/29/2004	Computerized method and system for documentation-based coding	EATON, JAMES D.
<u>11465856</u>	Not Issued	25	08/21/2006	SYSTEM AND METHOD FOR DISPLAYING DISCHARGE INSTRUCTIONS FOR A PATIENT	EATON, JAMES D.
<u>11465871</u>	Not Issued	25	08/21/2006	SYSTEM AND METHOD FOR COMPILING AND DISPLAYING DISCHARGE INSTRUCTIONS FOR A PATIENT	EATON, JAMES D.
<u>10232587</u>	<u>6759816</u>	150	08/30/2002	TAPE DRIVE WITH NON-CONTACT OPTICAL TAPE TENSION SENSOR	EATON, JAMES H.
<u>10876197</u>	<u>7092196</u>	150	06/23/2004	CHARACTERISTIC MEASUREMENT OF AN ACTUATION DRIVE COIL AND ASSOCIATED DAMPING MECHANISM	EATON, JAMES H.
<u>06372882</u>	<u>4493313</u>	250	04/29/1982	PARABOLIC TROUGH SOLAR COLLECTOR	EATON, JAMES H.
<u>06661540</u>	Not Issued	161	10/16/1984	PARABOLIC TROUGH SOLAR COLLECTOR	EATON, JAMES H.
<u>07628503</u>	Not Issued	166	12/17/1990	GIMBALED COMPLIANT GUIDE FOR TAPE DRIVES	EATON, JAMES H.
<u>07714952</u>	Not Issued	166	06/13/1991	COMPOUND RADIUS GUIDE POST	EATON, JAMES H.
<u>07714965</u>	<u>5289331</u>	250	06/13/1991	CONSTRAINED PIVOT GUIDE POST	EATON, JAMES H.
<u>07902094</u>	<u>5297754</u>	150	06/22/1992	TAPE REEL WITH BRAKE AND DUST SHIELD	EATON, JAMES H.
<u>07936714</u>	<u>5251844</u>	250	08/31/1992	GIMBALED COMPLIANT GUIDE FOR TAPE DRIVERS	EATON, JAMES H.
<u>07999478</u>	Not Issued	166	12/30/1992	RECESSED MAGNETORESISTIVE SERVO-POSITIONING HEAD FOR MAGNETIC TAPE DATA STORAGE SYSTEMS	EATON, JAMES H.
<u>08029284</u>	<u>5282105</u>	150	03/08/1993	COMPOUND RADIUS GUIDE POST	EATON, JAMES H.
<u>08074898</u>	Not Issued	166	06/10/1993	HIGH TORSIONAL STIFFNESS LOW AXIAL DEFLECTION STIFFNESS DRIVE SHAFT SYSTEM	EATON, JAMES H.

<u>08270207</u>	<u>5689384</u>	150	06/30/1994	TIMING BASED SERVO SYSTEM FOR MAGNETIC TAPE SYSTEMS	EATON, JAMES H.
<u>08328565</u>	Not Issued	161	10/24/1994	RECESSED MAGNETORESISTIVE SERVO-POSITIONING HEAD FOR MAGNETIC TAPE DATA STORAGE SYSTEM	EATON, JAMES H.
<u>08474646</u>	<u>5625510</u>	250	06/07/1995	HIGH TORSIONAL STIFFNESS LOW AXIAL DEFLECTION STIFFNESS DRIVE SHAFT	EATON, JAMES H.
<u>09641134</u>	<u>6754033</u>	150	08/16/2000	TAPE SURFACE CONSTRAINT OF LATERAL TRANSIENTS	EATON, JAMES HOWARD
<u>09711107</u>	<u>6320719</u>	150	11/09/2000	Timing based servo system for magnetic tape systems	EATON, JAMES HOWARD
<u>09711109</u>	<u>6462904</u>	150	11/09/2000	TIMING BASED SERVO SYSTEM FOR MAGNETIC TAPE SYSTEMS	EATON, JAMES HOWARD
<u>09877382</u>	<u>6646830</u>	150	06/07/2001	MONOLITHIC MAGNETIC READ-WHILE-WRITE HEAD APPARATUS AND METHOD OF MANUFACTURE	EATON, JAMES HOWARD
<u>10041864</u>	<u>6639760</u>	150	01/07/2002	COMPLIANT WORM GEAR AND WORM GEAR BRACKET	EATON, JAMES HOWARD
<u>10073290</u>	<u>6879457</u>	150	02/13/2002	TIMING BASED SERVO WITH FIXED DISTANCES BETWEEN TRANSITIONS	EATON, JAMES HOWARD
<u>10354834</u>	<u>6947256</u>	150	01/29/2003	EMBEDDED WIRE PLANAR WRITE HEAD SYSTEM AND METHOD	EATON, JAMES HOWARD
<u>10627079</u>	<u>7193812</u>	150	07/25/2003	RECORDING HEAD COMPENSATION FOR TAPE SHRINKAGE AND EXPANSION	EATON, JAMES HOWARD
<u>10638041</u>	<u>7002763</u>	150	08/08/2003	IDENTIFICATION OF Laterally Positioned SERVO BANDS EMPLOYING DIFFERENTIATING CHARACTERISTICS OF SERVO PATTERNS	EATON, JAMES HOWARD
<u>10638080</u>	<u>6937413</u>	150	08/08/2003	TAPE SERVO INFORMATION WITH SUPERIMPOSED DATA INFORMATION PROVIDING SERVO BAND IDENTIFICATION	EATON, JAMES HOWARD
<u>10672880</u>	Not	94	09/26/2003	METHOD OF MAKING A	EATON, JAMES

	Issued			MONOLITHIC MAGANETIC READ-WHILE-WRITE HEAD APPARATUS	HOWARD
10769271	Not Issued	41	01/30/2004	TAPE HEAD HAVING ONE OR MORE EMBEDDED CHIPS	EATON, JAMES HOWARD
10999383	Not Issued	93	11/30/2004	TRI-STATE SERVOWRITER DRIVER WITH SLOW RETURN TO ZERO	EATON, JAMES HOWARD
10999387	Not Issued	30	11/30/2004	Tape recording head promoting lateral motion	EATON, JAMES HOWARD
11172465	Not Issued	71	06/29/2005	Embedded wire planar write head system and method	EATON, JAMES HOWARD
11215132	7102846	150	08/29/2005	IDENTIFICATION OF LATERALLY POSITIONED SERVO BANDS EMPLOYING DIFFERENTIATING CHARACTERISTICS OF SERVO PATTERNS	EATON, JAMES HOWARD
11215133	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11215137	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11215410	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11215456	7102847	150	08/29/2005	IDENTIFICATION OF LATERALLY POSITIONED SERVO BANDS EMPLOYING DIFFERENTIATING CHARACTERISTICS OF SERVO PATTERNS	EATON, JAMES HOWARD

[Search and Display More Records.](#)

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="eaton"/>	<input type="text" value="james"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007
Time: 17:06:46

PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = PERNER

First Name = FREDERICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10146176	Not Issued	161	05/14/2002	Magnetic field detection sensor	PERNER, FREDERICK
10195818	6788605	150	07/15/2002	SHARED VOLATILE AND NON-VOLATILE MEMORY	PERNER, FREDERICK
10286081	6804145	150	11/01/2002	MEMORY CELL SENSING SYSTEM AND METHOD	PERNER, FREDERICK
10286577	6879534	150	11/01/2002	METHOD AND SYSTEM FOR MINIMIZING DIFFERENTIAL AMPLIFIER POWER SUPPLY SENSITIVITY	PERNER, FREDERICK
10299288	6765834	150	11/19/2002	SYSTEM AND METHOD FOR SENSING MEMORY CELLS OF AN ARRAY OF MEMORY CELLS	PERNER, FREDERICK
10452755	6826094	150	06/02/2003	MAGNETIC MEMORY CELL SENSING WITH FIRST AND SECOND CURRENTS	PERNER, FREDERICK
10678555	6987692	150	10/03/2003	MAGNETIC MEMORY HAVING ANGLED THIRD CONDUCTOR	PERNER, FREDERICK
10697367	6894918	150	10/30/2003	SHARED VOLATILE AND NON-VOLATILE MEMORY	PERNER, FREDERICK
11003204	Not Issued	167	12/03/2004	METHOD AND SYSTEM FOR MINIMIZING DIFFERENTIAL AMPLIFIER POWER SUPPLY SENSITIVITY	PERNER, FREDERICK
11129115	7023277	150	05/12/2005	METHOD AND SYSTEM FOR MINIMIZING DIFFERENTIAL AMPLIFIER POWER SUPPLY SENSITIVITY	PERNER, FREDERICK
09483383	6728799	150	01/13/2000	HYBRID DATA I/O FOR MEMORY APPLICATIONS	PERNER, FREDERICK A.
09498587	6185143	150	02/04/2000	MAGNETIC RANDOM ACCESS	PERNER,

				MEMORY (MRAM) DEVICE INCLUDING DIFFERENTIAL SENSE AMPLIFIERS	FREDERICK A.
<u>09498588</u>	<u>6584589</u>	150	02/04/2000	SELF-TESTING OF MAGNETO-RESISTIVE MEMORY ARRAYS	PERNER, FREDERICK A.
<u>09564308</u>	<u>6259644</u>	150	05/03/2000	Equipotential sense methods for resistive cross point memory cell arrays	PERNER, FREDERICK A.
<u>09564713</u>	<u>6256224</u>	150	05/03/2000	Write circuit for large mram arrays	PERNER, FREDERICK A.
<u>09652807</u>	<u>6317375</u>	150	08/31/2000	Method and apparatus for reading memory cells of a resistive cross point array	PERNER, FREDERICK A.
<u>09663016</u>	<u>6324093</u>	150	09/15/2000	Write-once thin-film memory	PERNER, FREDERICK A.
<u>09663752</u>	<u>6456525</u>	150	09/15/2000	SHORT-TOLERANT RESISTIVE CROSS POINT ARRAY	PERNER, FREDERICK A.
<u>09745103</u>	<u>6256247</u>	150	12/19/2000	Differential sense amplifiers for resistive cross point memory cell arrays	PERNER, FREDERICK A.
<u>09795608</u>	<u>7038242</u>	150	02/28/2001	AMORPHOUS SEMICONDUCTOR OPEN BASE PHOTOTRANSISTOR ARRAY	PERNER, FREDERICK A.
<u>09827114</u>	<u>6363000</u>	150	04/05/2001	Write circuit for large MRAM arrays	PERNER, FREDERICK A.
<u>09839012</u>	<u>6587384</u>	150	04/21/2001	MULTI-FUNCTION SERIAL I/O CIRCUIT	PERNER, FREDERICK A.
<u>09855118</u>	<u>6504779</u>	150	05/14/2001	RESISTIVE CROSS POINT MEMORY WITH ON-CHIP SENSE AMPLIFIER CALIBRATION METHOD AND APPARATUS	PERNER, FREDERICK A.
<u>09892251</u>	<u>6657358</u>	150	06/26/2001	POWER SUPPLY INCLUDING PYROELECTRIC CAPACITOR	PERNER, FREDERICK A.
<u>09912565</u>	<u>6462388</u>	150	07/26/2001	ISOLATION OF MEMORY CELLS IN CROSS POINT ARRAYS	PERNER, FREDERICK A.
<u>09915179</u>	Not Issued	161	07/25/2001	Fault tolerant magnetoresistive solid-state storage device	PERNER, FREDERICK A.
<u>09915194</u>	<u>7036068</u>	150	07/25/2001	ERROR CORRECTION CODING AND DECODING IN A SOLID-STATE STORAGE	PERNER, FREDERICK A.

				DEVICE	
<u>09921145</u>	<u>6564742</u>	150	08/03/2001	OVER-TEMPERATURE WARNING DEVICE	PERNER, FREDERICK A.
<u>09924577</u>	<u>6584029</u>	150	08/09/2001	ONE-TIME PROGRAMMABLE MEMORY USING FUSE/ANTI-FUSE AND VERTICALLY ORIENTED FUSE UNIT MEMORY CELLS	PERNER, FREDERICK A.
<u>09976304</u>	<u>6501697</u>	150	10/11/2001	HIGH DENSITY MEMORY SENSE AMPLIFIER	PERNER, FREDERICK A.
<u>09983701</u>	<u>6636436</u>	150	10/25/2001	ISOLATION OF MEMORY CELLS IN CROSS POINT ARRAYS	PERNER, FREDERICK A.
<u>09997199</u>	<u>7149948</u>	150	11/28/2001	MANUFACTURING TEST FOR A FAULT TOLERANT MAGNETORESISTIVE SOLID-STATE STORAGE DEVICE	PERNER, FREDERICK A.
<u>10000636</u>	<u>6456524</u>	150	10/31/2001	HYBRID RESISTIVE CROSS POINT MEMORY CELL ARRAYS AND METHODS OF MAKING THE SAME	PERNER, FREDERICK A.
<u>10005456</u>	<u>6586989</u>	150	11/06/2001	NONLINEAR DIGITAL DIFFERENTIAL AMPLIFIER OFFSET CALIBRATION	PERNER, FREDERICK A.
<u>10017567</u>	<u>6534841</u>	150	12/14/2001	CONTINUOUS ANTIFUSE MATERIAL IN MEMORY STRUCTURE	PERNER, FREDERICK A.
<u>10044724</u>	<u>6606262</u>	150	01/10/2002	MAGNETORESISTIVE RANDOM ACCESS MEMORY (MRAM) WITH ON-CHIP AUTOMATIC DETERMINATION OF OPTIMIZED WRITE CURRENT METHOD AND APPARATUS	PERNER, FREDERICK A.
<u>10055299</u>	<u>6650562</u>	150	01/23/2002	SYSTEM AND METHOD FOR DETERMINING THE LOGIC STATE OF A MEMORY CELL IN A MAGNETIC TUNNEL JUNCTION MEMORY DEVICE	PERNER, FREDERICK A.
<u>10079311</u>	<u>6665205</u>	150	02/20/2002	A SHARED GLOBAL WORD LINE MAGNETIC RANDOM ACCESS MEMORY	PERNER, FREDERICK A.
<u>10096542</u>	<u>6643213</u>	150	03/12/2002	WRITE PULSE CIRCUIT FOR A MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10107939</u>	<u>6848067</u>	150	03/27/2002	MULTI-PORT SCAN CHAIN	PERNER,

				REGISTER APPARATUS AND METHOD	FREDERICK A.
<u>10136782</u>	<u>6597598</u>	150	04/30/2002	RESISTIVE CROSS POINT MEMORY ARRAYS HAVING A CHARGE INJECTION DIFFERENTIAL SENSE AMPLIFIER	PERNER, FREDERICK A.
<u>10137475</u>	<u>6717234</u>	150	05/01/2002	RESISTIVE MEMORY FOR DATA STORAGE DEVICES	PERNER, FREDERICK A.
<u>10145834</u>	<u>6678200</u>	150	05/14/2002	SYSTEMS AND METHODS FOR COMMUNICATING WITH MEMORY BLOCKS	PERNER, FREDERICK A.
<u>10151913</u>	Not Issued	161	05/22/2002	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	PERNER, FREDERICK A.
<u>10151914</u>	<u>6801450</u>	150	05/22/2002	MEMORY CELL ISOLATION	PERNER, FREDERICK A.
<u>10151915</u>	<u>6757188</u>	150	05/22/2002	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	PERNER, FREDERICK A.
<u>10194767</u>	<u>6775196</u>	150	07/12/2002	MAGNETIC MEMORY HAVING A TEMPERATURE COMPENSATED WRITE CIRCUIT	PERNER, FREDERICK A.
<u>10198278</u>	<u>6590804</u>	150	07/16/2002	ADJUSTABLE CURRENT MODE DIFFERENTIAL AMPLIFIER	PERNER, FREDERICK A.
<u>10205531</u>	<u>6765819</u>	150	07/25/2002	MAGNETIC MEMORY DEVICE HAVING IMPROVED SWITCHING CHARACTERISTICS	PERNER, FREDERICK A.
<u>10218892</u>	<u>6717874</u>	150	08/14/2002	SYSTEMS AND METHODS FOR REDUCING THE EFFECT OF NOISE WHILE READING DATA IN SERIES FROM MEMORY	PERNER, FREDERICK A.

[Search and Display More Records.](#)

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="perner"/>	<input type="text" value="frederick"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007

Time: 17:06:51


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = PERNER

First Name = FREDERICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10223490	6728153	150	08/19/2002	MAGNETIC MEMORY WHICH COMPARES FIRST MEMORY CELL DATA BITS AND SECOND DATA BITS	PERNER, FREDERICK A.
10261532	6754123	150	10/01/2002	ADJUSTABLE CURRENT MODE DIFFERENTIAL AMPLIFIER FOR MULTIPLE BIAS POINT SENSING OF MRAM HAVING DIODE ISOLATION	PERNER, FREDERICK A.
10262051	6674679	150	10/01/2002	ADJUSTABLE CURRENT MODE DIFFERENTIAL AMPLIFIER FOR MULTIPLE BIAS POINT SENSING OF MRAM HAVING EQUI-POTENTIAL ISOLATION	PERNER, FREDERICK A.
10264588	6834017	150	10/03/2002	ERROR DETECTION SYSTEM FOR AN INFORMATION STORAGE DEVICE	PERNER, FREDERICK A.
10273623	6678197	150	10/18/2002	SYSTEMS AND METHODS FOR REDUCING THE EFFECT OF NOISE WHILE READING DATA FROM MEMORY	PERNER, FREDERICK A.
10289997	Not Issued	161	11/06/2002	Imaging system with non-volatile memory	PERNER, FREDERICK A.
10293027	6891768	150	11/13/2002	POWER-SAVING READING OF MAGNETIC MEMORY DEVICES	PERNER, FREDERICK A.
10346494	6839270	150	01/17/2003	SYSTEM FOR AND METHOD OF ACCESSING A FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY	PERNER, FREDERICK A.
10346700	6842389	150	01/17/2003	SYSTEM FOR AND METHOD OF FOUR-CONDUCTOR	PERNER, FREDERICK A.

				MAGNETIC RANDOM ACCESS MEMORY CELL AND DECODING SCHEME	
<u>10350398</u>	<u>6807118</u>	150	01/23/2003	ADJUSTABLE OFFSET DIFFERENTIAL AMPLIFIER	PERNER, FREDERICK A.
<u>10425362</u>	<u>6930914</u>	150	04/29/2003	METHODS FOR ISOLATING MEMORY ELEMENTS WITHIN AN ARRAY	PERNER, FREDERICK A.
<u>10426381</u>	<u>6667901</u>	150	04/29/2003	DUAL-JUNCTION MAGNETIC MEMORY DEVICE AND READ METHOD	PERNER, FREDERICK A.
<u>10444477</u>	<u>6956762</u>	150	05/23/2003	METHOD AND SYSTEM FOR DATA COMMUNICATION ON A CHIP	PERNER, FREDERICK A.
<u>10448574</u>	<u>6873543</u>	150	05/30/2003	MEMORY DEVICE	PERNER, FREDERICK A.
<u>10449572</u>	<u>7027318</u>	150	05/30/2003	METHOD AND SYSTEM FOR ADJUSTING OFFSET VOLTAGE	PERNER, FREDERICK A.
<u>10459635</u>	<u>6865104</u>	150	06/10/2003	MAGNETORESISTIVE RANDOM ACCESS MEMORY (MRAM) WITH ON-CHIP AUTOMATIC DETERMINATION OF OPTIMIZED WRITE CURRENT METHOD AND APPARATUS	PERNER, FREDERICK A.
<u>10601895</u>	<u>6865107</u>	150	06/23/2003	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
<u>10609278</u>	<u>6954373</u>	150	06/27/2003	APPARATUS AND METHOD FOR DETERMINING THE LOGIC STATE OF A MAGNETIC TUNNEL JUNCTION MEMORY DEVICE	PERNER, FREDERICK A.
<u>10614504</u>	<u>6836422</u>	150	07/07/2003	SYSTEM AND METHOD FOR READING A MEMORY CELL	PERNER, FREDERICK A.
<u>10614505</u>	<u>6842364</u>	150	07/07/2003	MEMORY CELL STRINGS IN A RESISTIVE CROSS POINT MEMORY CELL ARRAY	PERNER, FREDERICK A.
<u>10614581</u>	<u>6865108</u>	150	07/07/2003	MEMORY CELL STRINGS IN A RESISTIVE CROSS POINT MEMORY CELL ARRAY	PERNER, FREDERICK A.
<u>10623357</u>	<u>6898134</u>	150	07/18/2003	SYSTEMS AND METHODS FOR SENSING A MEMORY ELEMENT	PERNER, FREDERICK A.

<u>10625016</u>	<u>7079148</u>	150	07/23/2003	NON-VOLATILE MEMORY PARALLEL PROCESSOR	PERNER, FREDERICK A.
<u>10635399</u>	<u>6751147</u>	150	08/05/2003	METHOD FOR ADAPTIVELY WRITING A MAGNETIC RANDOM ACCESS MEMORY	PERNER, FREDERICK A.
<u>10635405</u>	<u>6826086</u>	150	08/05/2003	METHOD, APPARATUS AND SYSTEM FOR ERASING AND WRITING A MAGNETIC RANDOM ACCESS MEMORY	PERNER, FREDERICK A.
<u>10640792</u>	<u>6961262</u>	150	08/13/2003	MEMORY CELL ISOLATION	PERNER, FREDERICK A.
<u>10649076</u>	<u>6947313</u>	150	08/27/2003	METHOD AND APPARATUS OF COUPLING CONDUCTORS IN MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10649078</u>	<u>7221582</u>	150	08/27/2003	METHOD AND SYSTEM FOR CONTROLLING WRITE CURRENT IN MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10649752</u>	<u>6901005</u>	150	08/27/2003	METHOD AND SYSTEM READING MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10650278</u>	<u>7006388</u>	150	08/28/2003	MEMORY WITH REFERENCE- INITIATED SEQUENTIAL SENSING	PERNER, FREDERICK A.
<u>10656758</u>	<u>6927092</u>	150	09/05/2003	A METHOD OF FORMING A SHARED GLOBAL WORD LINE MRAM STRUCTURE	PERNER, FREDERICK A.
<u>10657385</u>	<u>6937509</u>	150	09/08/2003	DATA STORAGE DEVICE AND METHOD OF FORMING THE SAME	PERNER, FREDERICK A.
<u>10658442</u>	<u>6791873</u>	150	09/08/2003	APPARATUS AND METHOD FOR GENERATING A WRITE CURRENT FOR A MAGNETIC MEMORY CELL	PERNER, FREDERICK A.
<u>10660831</u>	<u>6999334</u>	150	09/12/2003	SYSTEM AND METHOD FOR DETERMINING THE LOGIC STATE OF A MEMORY CELL IN A MAGNETIC TUNNEL JUNCTION MEMORY DEVICE	PERNER, FREDERICK A.
<u>10661448</u>	Not Issued	30	09/11/2003	Increased magnetic memory array sizes and operating margins	PERNER, FREDERICK A.
<u>10675191</u>	<u>6842390</u>	150	09/30/2003	SYSTEMS AND METHODS FOR COMMUNICATING WITH MEMORY BLOCKS	PERNER, FREDERICK A.

<u>10675740</u>	<u>7079436</u>	150	09/30/2003	RESISTIVE CROSS POINT MEMORY	PERNER, FREDERICK A.
<u>10678322</u>	<u>6894938</u>	150	10/03/2003	SYSTEM AND METHOD OF CALIBRATING A READ CIRCUIT IN A MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10680464</u>	<u>6990012</u>	150	10/07/2003	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
<u>10680483</u>	<u>7136300</u>	150	10/06/2003	MAGNETIC MEMORY DEVICE INCLUDING GROUPS OF SERIES-CONNECTED MEMORY ELEMENTS	PERNER, FREDERICK A.
<u>10681483</u>	<u>6930370</u>	150	10/08/2003	MEMORY WITH CONDUCTORS BETWEEN OR IN COMMUNICATION WITH STORAGE UNITS	PERNER, FREDERICK A.
<u>10685618</u>	<u>6839271</u>	150	10/15/2003	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
<u>10689144</u>	<u>6847544</u>	150	10/20/2003	MAGNETIC MEMORY WHICH DETECTS CHANGES BETWEEN FIRST AND SECOND RESISTIVE STATES OF MEMORY CELL	PERNER, FREDERICK A.
<u>10690249</u>	<u>6990030</u>	150	10/21/2003	MAGNETIC MEMORY HAVING A CALIBRATION SYSTEM	PERNER, FREDERICK A.
<u>10696826</u>	<u>6873544</u>	150	10/30/2003	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	PERNER, FREDERICK A.
<u>10697776</u>	Not Issued	161	10/30/2003	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	PERNER, FREDERICK A.
<u>10698896</u>	Not Issued	164	10/31/2003	MULTI-SAMPLE READ CIRCUIT HAVING TEST MODE OF OPERATION	PERNER, FREDERICK A.
<u>10713510</u>	<u>6930369</u>	150	11/14/2003	THIN FILM DEVICE AND A METHOD OF PROVIDING THERMAL ASSISTANCE THEREIN	PERNER, FREDERICK A.
<u>10725746</u>	<u>6850430</u>	150	12/02/2003	REGULATING A MAGNETIC MEMORY CELL WRITE CURRENT	PERNER, FREDERICK A.
<u>10725803</u>	<u>6937504</u>	150	12/02/2003	SELECTING A MAGNETIC MEMORY CELL WRITE CURRENT	PERNER, FREDERICK A.

Search and Display More Records.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="perner"/>	<input type="text" value="frederick"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007
Time: 17:06:54

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = PERNER

First Name = FREDERICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10726393	6829191	150	12/03/2003	MAGNETIC MEMORY	PERNER, FREDERICK A.
10727273	6999366	150	12/03/2003	MAGNETIC MEMORY INCLUDING A SENSE RESULT CATEGORY BETWEEN LOGIC STATES	PERNER, FREDERICK A.
10752611	6777270	150	01/07/2004	RESISTIVE MEMORY FOR DATA STORAGE DEVICES	PERNER, FREDERICK A.
10753539	6937506	150	01/08/2004	MEGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
10754663	Not Issued	93	01/08/2004	ADDRESS CONTROL SYSTEM FOR A MEMORY STORAGE DEVICE	PERNER, FREDERICK A.
10764052	Not Issued	168	01/23/2004	Cross point resistive memory array	PERNER, FREDERICK A.
10765483	6982909	150	01/27/2004	SYSTEM AND METHOD FOR READING A MEMORY CELL	PERNER, FREDERICK A.
10765484	6914809	150	01/27/2004	MEMORY CELL STRINGS	PERNER, FREDERICK A.
10767428	Not Issued	95	01/29/2004	CURRENT THRESHOLD DETECTOR	PERNER, FREDERICK A.
10779909	7079438	150	02/17/2004	CONTROLLED TEMPERATURE, THERMAL- ASSISTED MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
10784514	6958933	150	02/23/2004	MEMORY CELL STRINGS	PERNER, FREDERICK A.
10794302	7042757	150	03/04/2004	1R1D MRAM BLOCK ARCHITECTURE	PERNER, FREDERICK A.
10806709	7102920	150	03/23/2004	SOFT-REFERENCE THREE CONDUCTOR MAGNETIC MEMORY STORAGE DEVICE	PERNER, FREDERICK A.

10814094	7002197	150	03/30/2004	CROSS POINT RESISTIVE MEMORY ARRAY	PERNER, FREDERICK A.
10832912	7057920	150	04/26/2004	TWO CONDUCTOR THERMALLY ASSISTED MAGNETIC MEMORY	PERNER, FREDERICK A.
10843787	7102921	150	05/11/2004	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
10854858	6940747	150	05/26/2004	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
10861035	Not Issued	41	06/04/2004	Light emitting device with adaptive intensity control	PERNER, FREDERICK A.
10861047	Not Issued	41	06/04/2004	Flexible media magnetic printing system	PERNER, FREDERICK A.
10897533	Not Issued	93	07/23/2004	METHOD OF MAKING ACTIVE MATRIX DISPLAY	PERNER, FREDERICK A.
10934243	Not Issued	71	09/03/2004	Method and apparatus for multi-plane MRAM	PERNER, FREDERICK A.
10934718	7102917	150	09/03/2004	MEMORY ARRAY METHOD AND SYSTEM	PERNER, FREDERICK A.
10934719	7130235	150	09/03/2004	METHOD AND APPARATUS FOR A SENSE AMPLIFIER	PERNER, FREDERICK A.
10957058	7116576	150	10/01/2004	SENSING THE STATE OF A STORAGE CELL INCLUDING A MAGNETIC ELEMENT	PERNER, FREDERICK A.
11003904	7145797	150	12/03/2004	SELECTING A MAGNETIC MEMORY CELL WRITE CURRENT	PERNER, FREDERICK A.
11089688	7180770	150	03/24/2005	SERIES DIODE THERMALLY ASSISTED MRAM	PERNER, FREDERICK A.
11112691	Not Issued	161	04/21/2005	Thin film device and a method of providing thermal assistance therein	PERNER, FREDERICK A.
11112795	Not Issued	94	04/21/2005	ACTIVE INTERCONNECTS AND CONTROL POINTS IN INTEGRATED CIRCUITS	PERNER, FREDERICK A.
11155523	7203265	150	06/16/2005	SYNCHRONOUS COUNTING CIRCUIT	PERNER, FREDERICK A.
11171694	7031185	150	06/30/2005	DATA STORAGE DEVICE AND METHOD OF FORMING THE SAME	PERNER, FREDERICK A.
11211986	7142448	150	08/25/2005	METHOD AND SYSTEM FOR DATA COMMUNICATION ON A CHIP	PERNER, FREDERICK A.
11252143	Not	89	10/17/2005	System and method for reading a	PERNER,

	Issued			memory cell	FREDERICK A.
<u>11264539</u>	Not Issued	30	11/01/2005	Controllably connectable strings of MRAM cells	PERNER, FREDERICK A.
<u>11266861</u>	Not Issued	93	11/03/2005	ANALOG PREAMPLIFIER CALIBRATION	PERNER, FREDERICK A.
<u>11267705</u>	Not Issued	30	11/03/2005	Digital current source	PERNER, FREDERICK A.
<u>07593703</u>	<u>5097206</u>	150	10/05/1990	BUILT-IN TEST CIRCUIT FOR STATIC CMOS CIRCUITS	PERNER, FREDERICK A.
<u>08694891</u>	<u>5818261</u>	150	08/08/1996	PSEUDO DIFFERENTIAL BUS DRIVER/RECEIVER FOR FIELD PROGRAMMABLE DEVICES	PERNER, FREDERICK A.
<u>08718849</u>	<u>5805477</u>	150	09/26/1996	ARITHMETIC CELL FOR FIELD PROGRAMMABLE DEVICES	PERNER, FREDERICK A.
<u>09030245</u>	<u>6115019</u>	150	02/25/1998	REGISTER PIXEL FOR LIQUID CRYSTAL DISPLAYS	PERNER, FREDERICK A.
<u>09057429</u>	<u>6552745</u>	150	04/08/1998	CMOS ACTIVE PIXEL WITH MEMORY FOR IMAGING SENSORS	PERNER, FREDERICK A.
<u>09072408</u>	<u>6227644</u>	150	05/04/1998	INKJET DOT IMAGING SENSOR FOR THE CALIBRATION OF INKJET PRINT HEADS	PERNER, FREDERICK A.
<u>09080996</u>	<u>5969399</u>	150	05/19/1998	HIGH GAIN CURRENT MODE PHOTO-SENSOR	PERNER, FREDERICK A.
<u>09099918</u>	<u>6246386</u>	150	06/18/1998	INTEGRATED MICRO-DISPLAY SYSTEM	PERNER, FREDERICK A.
<u>09174717</u>	<u>6114739</u>	150	10/19/1998	ELEVATED PIN DIODE ACTIVE PIXEL SENSOR WHICH INCLUDES A PATTERNED DOPED SEMICONDUCTOR ELECTRODE	PERNER, FREDERICK A.
<u>09174718</u>	<u>6018187</u>	150	10/19/1998	ELEVATED PIN DIODE ACTIVE PIXEL SENSOR INCLUDING A UNIQUE INTERCONNECTION STRUCTURE	PERNER, FREDERICK A.
<u>09184426</u>	<u>6545711</u>	150	11/02/1998	PHOTO DIODE PIXEL SENSOR ARRAY HAVING A GUARD RING	PERNER, FREDERICK A.
<u>09195032</u>	<u>6262703</u>	150	11/18/1998	PIXEL CELL WITH INTEGRATED DC BALANCE	PERNER, FREDERICK A.

				CIRCUIT	
09195588	6580454	150	11/18/1998	CMOS ACTIVE PIXEL SENSOR HAVING IN-PIXEL LOCAL EXPOSURE CONTROL	PERNER, FREDERICK A.
09203445	6111300	150	12/01/1998	MULTIPLE COLOR DETECTION ELEVATED PIN PHOTO DIODE ACTIVE PIXEL SENSOR	PERNER, FREDERICK A.
09306238	6051867	150	05/06/1999	INTERLAYER DIELECTRIC FOR PASSIVATION OF AN ELEVATED INTEGRATED CIRCUIT SENSOR STRUCTURE	PERNER, FREDERICK A.

[Search and Display More Records.](#)

Search Another: Inventor

Last Name

First Name

perner

frederick

Search

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007
Time: 17:07:02**PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = PERNER

First Name = FREDERICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09383106	Not Issued	161	08/25/1999	BIASING CONNECTION FOR AN ARRAY OF PHOTODIODES	PERNER, FREDERICK A.
09430238	6262625	150	10/29/1999	OPERATIONAL AMPLIFIER WITH DIGITAL OFFSET CALIBRATION	PERNER, FREDERICK A.
09430239	6128239	150	10/29/1999	MRAM DEVICE INCLUDING ANALOG SENSE AMPLIFIERS	PERNER, FREDERICK A.
09430611	6188615	150	10/29/1999	MRAM DEVICE INCLUDING DIGITAL SENSE AMPLIFIERS	PERNER, FREDERICK A.
10933039	7224598	150	09/02/2004	PROGRAMMING OF PROGRAMMABLE RESISTIVE MEMORY DEVICES	PERNER, FREDERICK A.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="perner"/>	<input type="text" value="frederick"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007

Time: 17:07:16


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = TRAN

First Name = LUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09498588	6584589	150	02/04/2000	SELF-TESTING OF MAGNETO-RESISTIVE MEMORY ARRAYS	TRAN, LUNG
09963933	6795281	150	09/25/2001	MAGNETO-RESISTIVE DEVICE INCLUDING SOFT SYNTHETIC FERRIMAGNET REFERENCE LAYER	TRAN, LUNG
09971347	6649423	150	10/04/2001	METHOD FOR MODIFYING SWITCHING FIELD CHARACTERISTICS OF MAGNETIC TUNNEL JUNCTIONS	TRAN, LUNG
10000636	6456524	150	10/31/2001	HYBRID RESISTIVE CROSS POINT MEMORY CELL ARRAYS AND METHODS OF MAKING THE SAME	TRAN, LUNG
10029694	6750491	150	12/20/2001	MAGNETIC MEMORY DEVICE HAVING SOFT REFERENCE LAYER	TRAN, LUNG
10233109	6803274	150	08/30/2002	MAGNETIC MEMORY CELL HAVING AN ANNULAR DATA LAYER AND A SOFT REFERENCE LAYER	TRAN, LUNG
10233115	6924539	150	08/30/2002	MAGNETIC MEMORY CELL HAVING AN ANNULAR DATA LAYER AND A SOFT REFERENCE LAYER	TRAN, LUNG
10299501	6781906	150	11/19/2002	MEMORY CELL SENSING INTEGRATOR	TRAN, LUNG
10346494	6839270	150	01/17/2003	SYSTEM FOR AND METHOD OF ACCESSING A FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY	TRAN, LUNG

<u>10346700</u>	<u>6842389</u>	150	01/17/2003	SYSTEM FOR AND METHOD OF FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY CELL AND DECODING SCHEME	TRAN, LUNG
<u>10624175</u>	<u>6906941</u>	150	07/22/2003	MAGNETIC MEMORY STRUCTURE	TRAN, LUNG
<u>10626447</u>	<u>6828610</u>	150	07/23/2003	METHOD FOR MODIFYING SWITCHING FIELD CHARACTERISTICS OF MAGNETIC TUNNEL JUNCTIONS	TRAN, LUNG
<u>10658158</u>	<u>6925003</u>	150	09/08/2003	MAGNETIC MEMORY CELL STRUCTURE	TRAN, LUNG
<u>10697191</u>	<u>6891212</u>	150	10/30/2003	MAGNETIC MEMORY DEVICE HAVING SOFT REFERENCE LAYER	TRAN, LUNG
<u>10832912</u>	<u>7057920</u>	150	04/26/2004	TWO CONDUCTOR THERMALLY ASSISTED MAGNETIC MEMORY	TRAN, LUNG
<u>10871761</u>	<u>7002820</u>	150	06/17/2004	SEMICONDUCTOR STORAGE DEVICE	TRAN, LUNG
<u>10902925</u>	<u>6903403</u>	150	07/30/2004	MAGNETIC MEMORY CELL HAVING AN ANNULAR DATA LAYER AND A SOFT REFERENCE LAYER	TRAN, LUNG
<u>10958007</u>	Not Issued	41	10/04/2004	Printed circuit board printing system and method	TRAN, LUNG
<u>11089688</u>	<u>7180770</u>	150	03/24/2005	SERIES DIODE THERMALLY ASSISTED MRAM	TRAN, LUNG
<u>11105054</u>	<u>7196957</u>	150	04/13/2005	MAGNETIC MEMORY STRUCTURE USING HEATER LINES TO ASSIST IN WRITING OPERATIONS	TRAN, LUNG
<u>11118828</u>	Not Issued	30	04/29/2005	Process for forming magnetic memory structures	TRAN, LUNG
<u>11285991</u>	Not Issued	30	11/23/2005	Multi-layered magnetic memory structures	TRAN, LUNG
<u>11286009</u>	Not Issued	30	11/23/2005	Multi-layered magnetic memory structures	TRAN, LUNG
<u>11286245</u>	Not Issued	30	11/23/2005	Multi-layered magnetic memory structures	TRAN, LUNG
<u>11531181</u>	Not Issued	30	09/12/2006	Semi-Permeable Membrane	TRAN, LUNG
<u>08974420</u>	<u>5930087</u>	150	11/20/1997	ROBUST RECORDING HEAD	TRAN, LUNG T.

				FOR NEAR-CONTACT OPERATION	
<u>09498587</u>	<u>6185143</u>	150	02/04/2000	MAGNETIC RANDOM ACCESS MEMORY (MRAM) DEVICE INCLUDING DIFFERENTIAL SENSE AMPLIFIERS	TRAN, LUNG T.
<u>09561317</u>	<u>6424565</u>	150	04/28/2000	SOLID-STATE MEMORY WITH MAGNETIC STORAGE CELLS	TRAN, LUNG T.
<u>09564308</u>	<u>6259644</u>	150	05/03/2000	Equipotential sense methods for resistive cross point memory cell arrays	TRAN, LUNG T.
<u>09564713</u>	<u>6256224</u>	150	05/03/2000	Write circuit for large mram arrays	TRAN, LUNG T.
<u>09598671</u>	<u>6317376</u>	150	06/20/2000	Reference signal generation for magnetic random access memory devices	TRAN, LUNG T.
<u>09758757</u>	<u>6603678</u>	150	01/11/2001	INFORMATION STORAGE DEVICE INCLUDING HEATING ELEMENTS FOR MAGNETIC MEMORY ELEMENTS	TRAN, LUNG T.
<u>09771857</u>	<u>6356477</u>	150	01/29/2001	Cross point memory array including shared devices for blocking sneak path currents	TRAN, LUNG T.
<u>09809707</u>	<u>6385111</u>	150	03/14/2001	REFERENCE SIGNAL GENERATION FOR MAGNETIC RANDOM ACCESS MEMORY DEVICES	TRAN, LUNG T.
<u>09825093</u>	<u>6538920</u>	150	04/02/2001	CLADDED READ CONDUCTOR FOR A PINNED-ON-THE-FLY SOFT REFERENCE LAYER	TRAN, LUNG T.
<u>09827114</u>	<u>6363000</u>	150	04/05/2001	Write circuit for large MRAM arrays	TRAN, LUNG T.
<u>09871386</u>	Not Issued	161	05/31/2001	Solid-state memory with magnetic storage cells	TRAN, LUNG T.
<u>09871387</u>	<u>6607924</u>	150	05/31/2001	SOLID-STATE MEMORY WITH MAGNETIC STORAGE CELLS	TRAN, LUNG T.
<u>09908901</u>	<u>6434048</u>	150	07/20/2001	PULSE TRAIN WRITING OF WORM STORAGE DEVICE	TRAN, LUNG T.
<u>09910823</u>	<u>6515896</u>	150	07/24/2001	MEMORY DEVICE WITH SHORT READ TIME	TRAN, LUNG T.
<u>09917882</u>	<u>6434060</u>	150	07/31/2001	WRITE PULSE LIMITING FOR WORM STORAGE DEVICE	TRAN, LUNG T.
<u>09924500</u>	<u>6567301</u>	150	08/09/2001	ONE-TIME PROGRAMMABLE UNIT MEMORY CELL BASED ON VERTICALLY ORIENTED	TRAN, LUNG T.

				FUSE AND DIODE AND ONE-TIME PROGRAMMABLE MEMORY USING THE SAME	
09924577	6584029	150	08/09/2001	ONE-TIME PROGRAMMABLE MEMORY USING FUSE/ANTI-FUSE AND VERTICALLY ORIENTED FUSE UNIT MEMORY CELLS	TRAN, LUNG T.
09944680	6385079	150	08/31/2001	METHODS AND STRUCTURE FOR MAXIMIZING SIGNAL TO NOISE RATIO IN RESISTIVE ARRAY	TRAN, LUNG T.
09951378	6541792	250	09/14/2001	MEMORY DEVICE HAVING DUAL TUNNEL JUNCTION MEMORY CELLS	TRAN, LUNG T.
09963171	6576969	150	09/25/2001	MAGNETO-RESISTIVE DEVICE HAVING SOFT REFERENCE LAYER	TRAN, LUNG T.
09963851	6538917	150	09/25/2001	READ METHODS FOR MAGNETO-RESISTIVE DEVICE HAVING SOFT REFERENCE LAYER	TRAN, LUNG T.
09963932	6504221	150	09/25/2001	MAGNETO-RESISTIVE DEVICE INCLUDING SOFT REFERENCE LAYER HAVING EMBEDDED CONDUCTORS	TRAN, LUNG T.
09983404	6473337	250	10/24/2001	MEMORY DEVICE HAVING MEMORY CELLS WITH MAGNETIC TUNNEL JUNCTION AND TUNNEL JUNCTION IN SERIES	TRAN, LUNG T.
09983697	6639859	150	10/25/2001	TEST ARRAY AND METHOD FOR TESTING MEMORY ARRAYS	TRAN, LUNG T.

[Search and Display More Records.](#)

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="tran"/>	<input type="text" value="lung"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007

Time: 17:07:22


PALM INTRANET
Inventor Name Search Result

Your Search was:

Last Name = TRAN

First Name = LUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09984934	6504742	150	10/31/2001	3-D-MEMORY DEVICE FOR LARGE STORAGE CAPACITY	TRAN, LUNG T.
09992426	6483734	150	11/26/2001	MEMORY DEVICE HAVING MEMORY CELLS CAPABLE OF FOUR STATES	TRAN, LUNG T.
09998216	6608790	150	12/03/2001	WRITE CURRENT COMPENSATION FOR TEMPERATURE VARIATIONS IN MEMORY ARRAYS	TRAN, LUNG T.
10084111	6678189	150	02/25/2002	METHOD AND SYSTEM FOR PERFORMING EQUIPOTENTIAL SENSING ACROSS A MEMORY ARRAY TO ELIMINATE LEAKAGE CURRENTS	TRAN, LUNG T.
10098204	6593608	150	03/15/2002	MAGNETO RESISTIVE STORAGE DEVICE HAVING DOUBLE TUNNEL JUNCTION	TRAN, LUNG T.
10098206	6885573	150	03/15/2002	DIODE FOR USE IN MRAM DEVICES AND METHOD OF MANUFACTURE	TRAN, LUNG T.
10098903	6778421	150	03/14/2002	MEMORY DEVICE ARRAY HAVING A PAIR OF MAGNETIC BITS SHARING A COMMON CONDUCTOR LINE	TRAN, LUNG T.
10116497	6967350	150	04/02/2002	MEMORY STRUCTURES	TRAN, LUNG T.
10133605	6673714	150	04/25/2002	METHOD OF FABRICATING A SUB-LITHOGRAPHIC SIZED VIA	TRAN, LUNG T.
10136782	6597598	150	04/30/2002	RESISTIVE CROSS POINT MEMORY ARRAYS HAVING A CHARGE INJECTION	TRAN, LUNG T.

				DIFFERENTIAL SENSE AMPLIFIER	
<u>10136976</u>	<u>6574129</u>	150	04/30/2002	RESISTIVE CROSS POINT MEMEORY CELL ARRAYS HAVING A CROSS-COUPLE LATCH SENSE AMPLIFIER	TRAN, LUNG T.
<u>10151913</u>	Not Issued	161	05/22/2002	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	TRAN, LUNG T.
<u>10151915</u>	<u>6757188</u>	150	05/22/2002	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	TRAN, LUNG T.
<u>10232363</u>	<u>6791865</u>	150	09/03/2002	MEMORY DEVICE CAPABLE OF CALIBRATION AND CALIBRATION METHODS THEREFOR	TRAN, LUNG T.
<u>10234511</u>	<u>6754097</u>	150	09/03/2002	READ OPERATIONS ON MULTI-BIT MEMORY CELLS IN RESISTIVE CROSS POINT ARRAYS	TRAN, LUNG T.
<u>10235011</u>	<u>6577529</u>	150	09/03/2002	MULTI-BIT MAGNETIC MEMORY DEVICE	TRAN, LUNG T.
<u>10235045</u>	<u>6801451</u>	150	09/03/2002	MAGNETIC MEMORY DEVICES HAVING MULTIPLE BITS PER MEMORY CELL	TRAN, LUNG T.
<u>10254559</u>	<u>6577549</u>	150	09/25/2002	WRITE CURRENT COMPENSATION FOR TEMPERATURE VARIATIONS IN MEMORY ARRAYS	TRAN, LUNG T.
<u>10299542</u>	<u>6791867</u>	150	11/18/2002	SELECTION OF MEMORY CELLS IN DATA STORAGE DEVICES	TRAN, LUNG T.
<u>10315748</u>	Not Issued	71	12/10/2002	THERMALLY-ASSISTED SWITCHING OF MAGNETIC MEMORY ELEMENTS	TRAN, LUNG T.
<u>10349685</u>	<u>6693825</u>	150	01/23/2003	MAGNETO-RESISTIVE DEVICE INCLUDING CLAD CONDUCTOR	TRAN, LUNG T.
<u>10351013</u>	<u>6891746</u>	150	01/25/2003	MAGNETO-RESISTIVE DEVICE HAVING SOFT REFERENCE LAYER	TRAN, LUNG T.
<u>10459437</u>	<u>6826079</u>	150	06/10/2003	METHOD AND SYSTEM FOR PERFORMING EQUIPOTENTIAL SENSING ACROSS A MEMORY ARRAY	TRAN, LUNG T.

				TO ELIMINATE LEAKAGE CURRENTS	
<u>10465714</u>	<u>7027319</u>	150	06/19/2003	RETRIEVING DATA STORED IN A MAGNETIC INTEGRATED MEMORY	TRAN, LUNG T.
<u>10657519</u>	<u>6961263</u>	150	09/08/2003	MEMORY DEVICE WITH A THERMALLY ASSISTED WRITE	TRAN, LUNG T.
<u>10661448</u>	Not Issued	30	09/11/2003	Increased magnetic memory array sizes and operating margins	TRAN, LUNG T.
<u>10668405</u>	<u>6865105</u>	150	09/22/2003	THERMAL-ASSISTED SWITCHING ARRAY CONFIGURATION FOR MRAM	TRAN, LUNG T.
<u>10692617</u>	<u>6879508</u>	150	10/24/2003	MEMORY DEVICE ARRAY HAVING A PAIR OF MAGNETIC BITS SHARING A COMMON CONDUCTOR LINE	TRAN, LUNG T.
<u>10695567</u>	<u>7038878</u>	150	10/28/2003	MICROMOVER CONFIGURED TO MOVE A MAGNETIC STORAGE MEDIUM IN PERPENDICULAR DIRECTIONS IN A PLANE	TRAN, LUNG T.
<u>10695710</u>	<u>7057258</u>	150	10/29/2003	RESISTIVE MEMORY DEVICE AND METHOD FOR MAKING THE SAME	TRAN, LUNG T.
<u>10696826</u>	<u>6873544</u>	150	10/30/2003	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	TRAN, LUNG T.
<u>10697776</u>	Not Issued	161	10/30/2003	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	TRAN, LUNG T.
<u>10753298</u>	<u>6791874</u>	150	01/08/2004	MEMORY DEVICE CAPABLE OF CALIBRATION AND CALIBRATION METHODS THEREFOR	TRAN, LUNG T.
<u>10969085</u>	Not Issued	30	10/20/2004	Display device	TRAN, LUNG T.
<u>11089687</u>	Not Issued	161	03/24/2005	Method of manufacture for improved diode for use in MRAM devices	TRAN, LUNG T.
<u>08614981</u>	Not Issued	161	03/12/1996	ESD PROTECTION DEVICE FOR MAGNETIC RECORDING HEAD	TRAN, LUNG T.
<u>08974925</u>	<u>6169686</u>	150	11/20/1997	SOLID-STATE MEMORY WITH	TRAN, LUNG T.

				MAGNETIC STORAGE CELLS	
<u>09334485</u>	<u>6111783</u>	150	06/16/1999	MRAM DEVICE INCLUDING WRITE CIRCUIT FOR SUPPLYING WORD AND BIT LINE CURRENT HAVING UNEQUAL MAGNITUDES	TRAN, LUNG T.
<u>09370087</u>	<u>6163477</u>	150	08/06/1999	MRAM DEVICE USING MAGNETIC FIELD BIAS TO IMPROVE REPRODUCIBILITY OF MEMORY CELL SWITCHING	TRAN, LUNG T.
<u>09430238</u>	<u>6262625</u>	150	10/29/1999	OPERATIONAL AMPLIFIER WITH DIGITAL OFFSET CALIBRATION	TRAN, LUNG T.
<u>09430611</u>	<u>6188615</u>	150	10/29/1999	MRAM DEVICE INCLUDING DIGITAL SENSE AMPLIFIERS	TRAN, LUNG T.
<u>10093344</u>	<u>6625059</u>	150	03/06/2002	SYNTHETIC FERRIMAGNET REFERENCE LAYER FOR A MAGNETIC STORAGE DEVICE	TRAN, LUNG THE
<u>10176179</u>	<u>6507513</u>	150	06/20/2002	USING DELAYED ELECTRICAL PULSES WITH MAGNETO-RESISTIVE DEVICES	TRAN, LUNG THE
<u>10680483</u>	<u>7136300</u>	150	10/06/2003	MAGNETIC MEMORY DEVICE INCLUDING GROUPS OF SERIES-CONNECTED MEMORY ELEMENTS	TRAN, LUNG THE
<u>07209651</u>	<u>4878140</u>	150	06/21/1988	MAGNETO-RESISTIVE SENSOR WITH OPPOSING CURRENTS FOR READING PERPENDICULARLY RECORDED MEDIA	TRAN, LUNG THE
<u>10196877</u>	<u>6850433</u>	150	07/15/2002	MAGNETIC MEMORY DEVICE AND METHOD	TRAN, LUNG THE

Inventor Search Completed: No Records to Display.

Search Another: Inventor
Last Name
First Name

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday

Date: 6/7/2007
Time: 17:07:52 **PALM INTRANET****Inventor Name Search Result**

Your Search was:

Last Name = ELDREDGE

First Name = KENNETH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10743662	Not Issued	93	12/22/2003	MRAM WITH CONTROLLER	ELDREDGE, KENNETH
09498587	6185143	150	02/04/2000	MAGNETIC RANDOM ACCESS MEMORY (MRAM) DEVICE INCLUDING DIFFERENTIAL SENSE AMPLIFIERS	ELDREDGE, KENNETH I.
09483383	6728799	150	01/13/2000	HYBRID DATA I/O FOR MEMORY APPLICATIONS	ELDREDGE, KENNETH J.
09498588	6584589	150	02/04/2000	SELF-TESTING OF MAGNETO-RESISTIVE MEMORY ARRAYS	ELDREDGE, KENNETH J.
09564713	6256224	150	05/03/2000	Write circuit for large mram arrays	ELDREDGE, KENNETH J.
09598671	6317376	150	06/20/2000	Reference signal generation for magnetic random access memory devices	ELDREDGE, KENNETH J.
09759952	6782616	150	01/12/2001	CONNECTION ARRANGEMENTS FOR ELECTRICAL DEVICES	ELDREDGE, KENNETH J.
09800561	6590850	150	03/07/2001	PACKAGING FOR STORAGE DEVICES USING ELECTRON EMISSIONS	ELDREDGE, KENNETH J.
09809707	6385111	150	03/14/2001	REFERENCE SIGNAL GENERATION FOR MAGNETIC RANDOM ACCESS MEMORY DEVICES	ELDREDGE, KENNETH J.
09811299	6791931	150	03/16/2001	ACCELEROMETER USING FIELD EMITTER TECHNOLOGY	ELDREDGE, KENNETH J.
09827114	6363000	150	04/05/2001	Write circuit for large MRAM arrays	ELDREDGE, KENNETH J.
09915179	Not	161	07/25/2001	Fault tolerant magnetoresistive	ELDREDGE,

	Issued			solid-state storage device	KENNETH J.
<u>09922436</u>	<u>6671778</u>	150	08/03/2001	REDUNDANT ARRAY OF INDEPENDENT STORAGE DEVICES	ELDREDGE, KENNETH J.
<u>10017567</u>	<u>6534841</u>	150	12/14/2001	CONTINUOUS ANTIFUSE MATERIAL IN MEMORY STRUCTURE	ELDREDGE, KENNETH J.
<u>10044542</u>	<u>6643195</u>	150	01/11/2002	SELF-HEALING MRAM	ELDREDGE, KENNETH J.
<u>10051476</u>	<u>6775223</u>	150	01/17/2002	SYSTEMS FOR CONTROLLING STORAGE DEVICE EMITTERS	ELDREDGE, KENNETH J.
<u>10120113</u>	<u>6625055</u>	150	04/09/2002	MULTIPLE LOGICAL BITS PER MEMORY CELL IN A MEMORY DEVICE	ELDREDGE, KENNETH J.
<u>10120118</u>	Not Issued	161	04/09/2002	Non-volatile, multi-level memory device	ELDREDGE, KENNETH J.
<u>10151915</u>	<u>6757188</u>	150	05/22/2002	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	ELDREDGE, KENNETH J.
<u>10295651</u>	Not Issued	41	11/15/2002	Transferring data in selectable transfer modes	ELDREDGE, KENNETH J.
<u>10314114</u>	<u>6836429</u>	150	12/07/2002	MRAM HAVING TWO WRITE CONDUCTORS	ELDREDGE, KENNETH J.
<u>10384053</u>	Not Issued	164	03/06/2003	ASSISTED MEMORY SYSTEM	ELDREDGE, KENNETH J.
<u>10609904</u>	<u>6906964</u>	150	06/27/2003	MULTIPLE BUFFER MEMORY INTERFACE	ELDREDGE, KENNETH J.
<u>10611544</u>	<u>7019381</u>	150	07/01/2003	METHOD OF PROVIDING MULTIPLE LOGICAL BITS PER MEMORY CELL	ELDREDGE, KENNETH J.
<u>10661448</u>	Not Issued	30	09/11/2003	Increased magnetic memory array sizes and operating margins	ELDREDGE, KENNETH J.
<u>10676465</u>	<u>6927996</u>	150	09/30/2003	MAGNETIC MEMORY DEVICE	ELDREDGE, KENNETH J.
<u>10695363</u>	<u>7213086</u>	150	10/28/2003	SYSTEM HAVING A STORAGE CONTROLLER THAT MODIFIES OPERATION OF A STORAGE SYSTEM BASED ON THE STATUS OF A DATA TRANSFER	ELDREDGE, KENNETH J.
<u>10696826</u>	<u>6873544</u>	150	10/30/2003	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM)	ELDREDGE, KENNETH J.

				WITH SERIES DIODES	
10727402	6982571	150	12/04/2003	SYSTEMS AND METHODS FOR TRANSLATING VOLTAGE LEVELS OF DIGITAL SIGNALS	ELDREDGE, KENNETH J.
10792264	6916182	150	03/03/2004	Connection arrangements for electrical devices having a ledge on which contact terminals are provided	ELDREDGE, KENNETH J.
10983776	7102918	150	11/07/2004	MRAM HAVING TWO WRITE CONDUCTORS	ELDREDGE, KENNETH J.
11264539	Not Issued	30	11/01/2005	Controllably connectable strings of MRAM cells	ELDREDGE, KENNETH J.
60835624	Not Issued	20	08/05/2006	Flash hard drive and method	ELDREDGE, KENNETH J.
60886926	Not Issued	20	01/27/2007	Solid State Storage Element and Method	ELDREDGE, KENNETH J.
08589674	Not Issued	161	01/22/1996	APPARATUS AND METHOD FOR THERMAL ASPERITY DETECTION AND FAILURE PREDICTION IN DISK DRIVE SYSTEMS	ELDREDGE, KENNETH J.
09430238	6262625	150	10/29/1999	OPERATIONAL AMPLIFIER WITH DIGITAL OFFSET CALIBRATION	ELDREDGE, KENNETH J.
09430611	6188615	150	10/29/1999	MRAM DEVICE INCLUDING DIGITAL SENSE AMPLIFIERS	ELDREDGE, KENNETH J.
10448574	6873543	150	05/30/2003	MEMORY DEVICE	ELDREDGE, KENNETH JAMES
10454463	6839275	150	06/04/2003	MEMORY SYSTEM	ELDREDGE, KENNETH JAMES
10695567	7038878	150	10/28/2003	MICROMOVER CONFIGURED TO MOVE A MAGNETIC STORAGE MEDIUM IN PERPENDICULAR DIRECTIONS IN A PLANE	ELDREDGE, KENNETH JAMES
10718137	6967149	150	11/20/2003	STORAGE STRUCTURE WITH CLEAVED LAYER	ELDREDGE, KENNETH JAMES

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="eldredge"/>	<input type="text" value="kenneth"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)